



FIG. 3

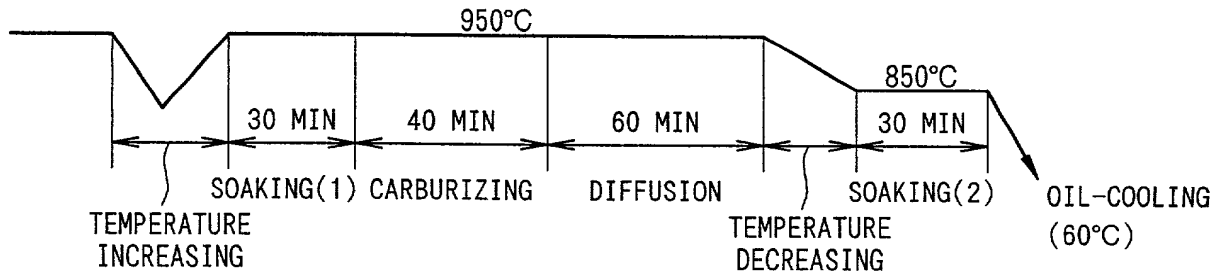


FIG. 4

SYSTEM BLOCK DIAGRAM

